

FIGURE 1

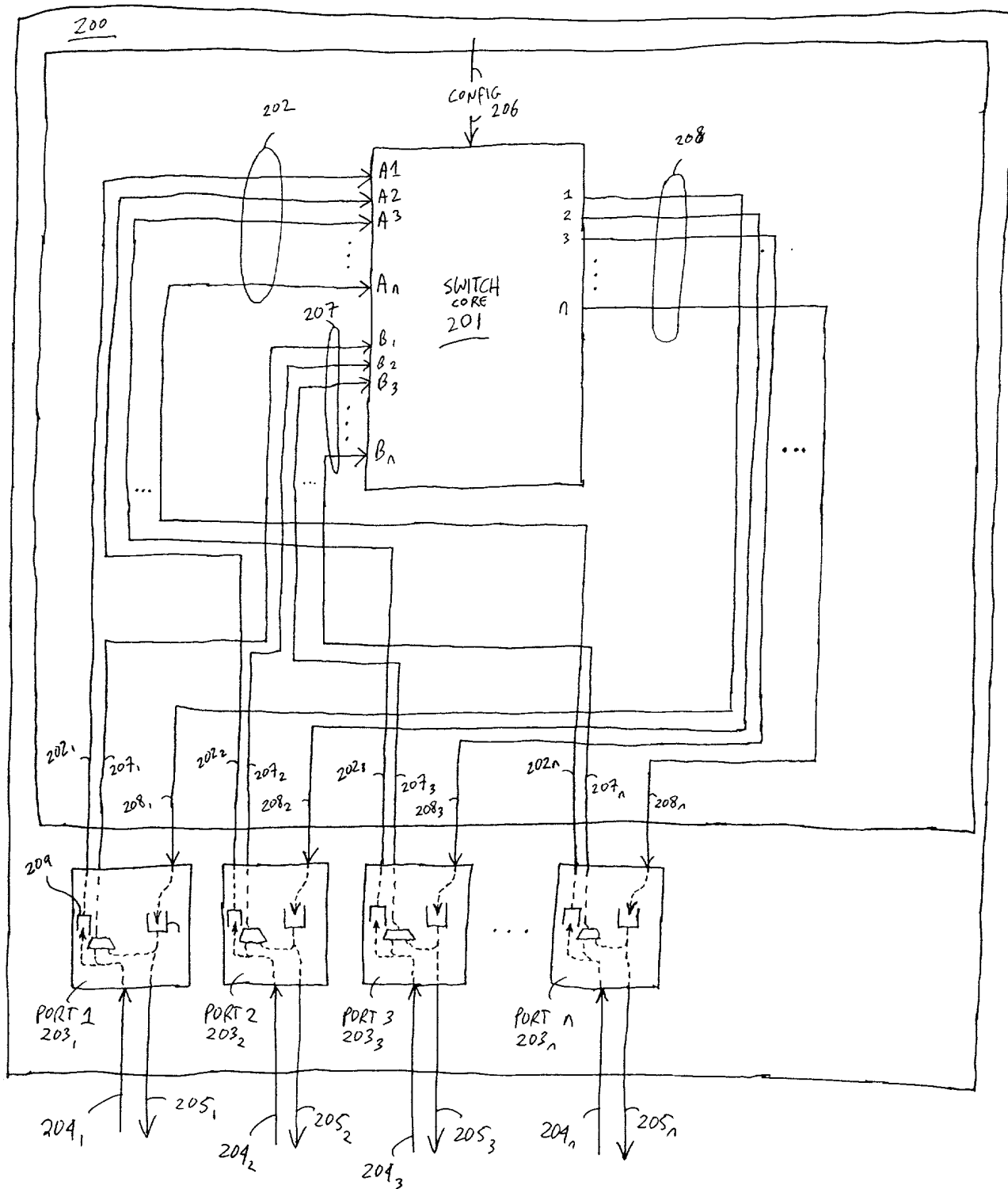


FIGURE 2

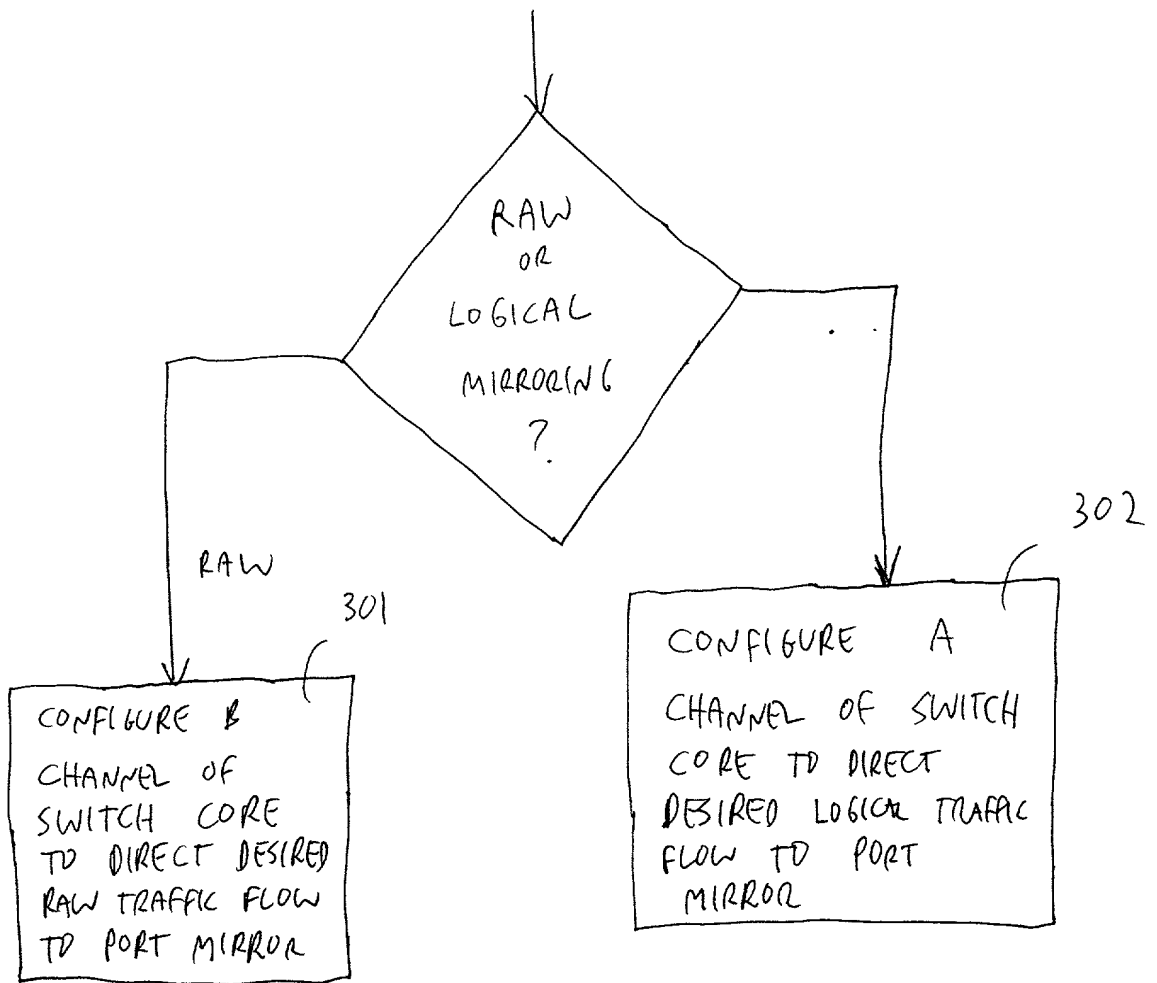


FIGURE 3

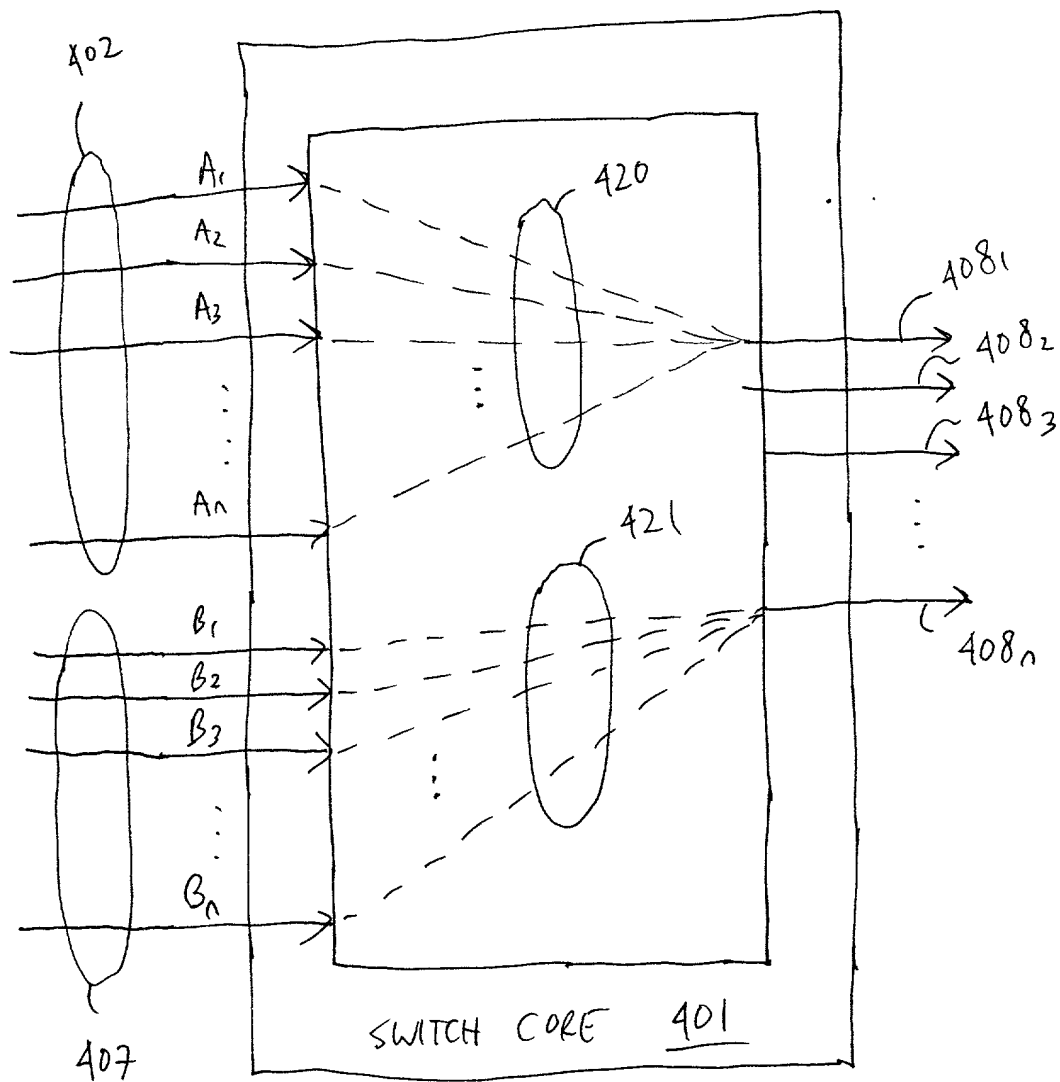


FIGURE 4

A hand-drawn schematic diagram of a circuit, likely representing a logic gate or a small processor component. The diagram is enclosed in a rectangular box labeled S03. The circuit includes several components and connections:

- Inputs/Outputs:**
 - S01:** An input signal entering from the bottom left.
 - S02:** An output signal exiting from the top left.
 - S04:** An input signal entering from the bottom left, below S01.
 - S05:** An input signal entering from the bottom right.
 - S07:** An input signal entering from the top center.
- Internal Components:**
 - S10:** A component (possibly a buffer or inverter) connected to S01 and S04.
 - S11:** A component (possibly a buffer or inverter) connected to S05.
 - S12:** A component (possibly a buffer or inverter) connected to S11.
 - S13:** A component (possibly a buffer or inverter) connected to S07.
 - S14:** A component (possibly a buffer or inverter) connected to S13.
 - S15:** A component (possibly a buffer or inverter) connected to S05.
- Connections:**
 - A horizontal line connects S10 and S11.
 - A vertical line connects S13 and S14.
 - A horizontal line connects S14 and S12.
 - A horizontal line connects S11 and S12.
 - A horizontal line connects S13 and S14.
 - A horizontal line connects S14 and S12.
 - A horizontal line connects S11 and S12.
 - A horizontal line connects S13 and S14.
 - A horizontal line connects S14 and S12.
 - A horizontal line connects S11 and S12.

Figure 5